OCT 2 6 2004

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#### PLEASE DELIVER THE FOLLOWING PAGES IMMEDIATELY TO:

NAME:

EXAMINER G. C. RAY, GROUP ART UNIT 2111

FIRM:

U.S. PATENT AND TRADEMARK OFFICE

CITY:

ALEXANDRIA, VIRGINIA

FAX NO:

(703) 872-9306

REMARKS: Serial No. 10/034,464, filed 12/28/2001

Attached hereto is a Response to Office Action dated August 26, 2004, for filing with the U.S. Patent and Trademark Office. Please acknowledge

receipt of this facsimile.

Total Number of Pages (Including This One): TWENTY-SEVEN (27)

FROM: Mark E. Scott, Direct Dial No. (713) 238-8049

DATE: October 26, 2004

CLIENT/MATTER NO. 200304307-1 (1662-50000)

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HEWLETT-PACKARD COMPANY Intellectual Property Administration P. O. Box 272400 Fort Callins, Colorado 80527-2400

# ORIGINAL

PATENT APPLICATION

ATTORNEY DOCKET NO. 200304307-1

### UNITED STATES PATENT AND TRADEMARK OFFICE

Thomas J. BONOLA et al. Inventor(s):

Confirmation No.: 9354

Application No.: 10/034,464

Examiner: G. C. Ray

Filing Date:

12/28/2001

Group Art Unit: 2111

Title:

METHOD AND APPARATUS FOR ELIMINATING THE SOFTWARE GENERATED

READY-SIGNAL TO HARDWARE DEVICES THAT ARE NOT PART OF THE MEMORY

COHERENCY DOMAIN

Mall Stop Amendment **Commissioner For Patents** P A

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Signature:	001	low to	4	LOU.	m.	Att	torney/	Agen	t for	Applica	ant(s)	

43,100 Reg. No.

Date: 10/26/2004

HEWLETT-PACKARD COMPANY Intellectual Property Administration P. O. Box 272400 Fort Collins, Colorado 80527-2400



#### PATENT APPLICATION

ATTORNEY DOCKET NO. \_\_\_\_ 200304307-1

#### IN TH

#### UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s):

Thomas J. BONOLA et al.

Confirmation No.: 9354

Application No.: 10/034,464

Examiner: G. C. Ray

Filing Date:

12/28/2001

Group Art Unit: 2111

Title:

Sir:

METHOD AND APPARATUS FOR ELIMINATING THE SOFTWARE GENERATED

READY-SIGNAL TO HARDWARE DEVICES THAT ARE NOT PART OF THE MEMORY

COHERENCY DOMAIN

Mail Stop Amendment Commissioner For Patents PO Box 1450 Alexandria, VA 22313-1450

### TRANSMITTAL LETTER FOR RESPONSE/AMENDMENT

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Charge \$ 284 to Deposit Account 08-2025. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25. Additionally please charge any fees to Deposit Account 08-2025 under 37 CFR 1.16, 1.17, 1.19, 1.20 and 1.21. A duplicate copy of this sheet is enclosed.

Typed Name: Gollean F. Brown

Superior College Filston

Mark E. Scott

Respectfully safe

Attorney/Agent for Applicant(s)

Reg. No. 43,100

Date: 10/26/2004

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### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Thomas J. BONOLA et al. § Applicants: *തതതതതതതതതത*  Confirmation No.:

9354

Serial No.:

10/034,464

Group Art Unit:

2111

Filed:

12/28/2001

Examiner:

G. C. Ray

For:

Method And Apparatus For Eliminating The Software Generated Ready-Signal To Hardware Devices That Are Not Part Of The Memory Coherency Domain

Docket No.:

200304307-1

# RESPONSE TO OFFICE ACTION DATED AUGUST 26, 2004

Mall Stop Amendment Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450 Date: October 26, 2004

#### **AMENDMENT**

Sir:

In response to the Office action of August 26, 2004, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 6 of this paper.

Remarks/Arguments begin on page 21 of this paper.